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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/734,082	12/10/2003	Ramesh Yarlagadda	23019-07460	7970	
758 7590 01/16/2007 FENWICK & WEST LLP			EXAMINER		
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801 CALIFOR MOUNTAIN V	NIA STREET VIEW, CA 94041	• • •	ART UNIT	PAPER NUMBER	
			2189		
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SHORTENED STATUTOR	RY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE		
3 MO	NTHS	01/16/2007	PAPER		

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

		Application No.	No. Applicant(s)			
		10/734,082	YARLAGADDA ET AL.			
	Office Action Summary	Examiner	Art Unit	_		
- 		Woo H. Choi	2189			
Period fo	The MAILING DATE of this communication approximation ap	opears on the cover sheet with the c	orrespondence address			
WHIC - Exter after - If NO - Failu Any r	ORTENED STATUTORY PERIOD FOR REP CHEVER IS LONGER, FROM THE MAILING I assions of time may be available under the provisions of 37 CFR 1 SIX (6) MONTHS from the mailing date of this communication, period for reply is specified above, the maximum statutory period to reply within the set or extended period for reply will, by statutely received by the Office later than three months after the mailed patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNICATION 1.136(a). In no event, however, may a reply be timed d will apply and will expire SIX (6) MONTHS from the cause the application to become ABANDONE	I. lely filed the mailing date of this communication. D (35 U.S.C. § 133).			
Status						
1)🖂	Responsive to communication(s) filed on <u>21 September 2006</u> .					
3)						
,	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Dispositi	on of Claims					
4)⊠	4)⊠ Claim(s) <u>1-17</u> is/are pending in the application.					
	4a) Of the above claim(s) is/are withdrawn from consideration.					
	Claim(s) is/are allowed.					
·	☐ Claim(s) is/are allowed. ☐ Claim(s) <u>1-17</u> is/are rejected.					
	Claim(s) is/are objected to.					
,	Claim(s) are subject to restriction and	or election requirement				
	,	or election requirement.				
	on Papers					
	9) The specification is objected to by the Examiner.					
10)[The drawing(s) filed on is/are: a)☐ ac	ccepted or b) \square objected to by the ${ t I}$	Examiner.			
	Applicant may not request that any objection to the	e drawing(s) be held in abeyance. See	e 37 CFR 1.85(a).			
_	Replacement drawing sheet(s) including the corre	• • • • • • • • • • • • • • • • • • • •				
11)[The oath or declaration is objected to by the E	Examiner. Note the attached Office	Action or form PTO-152.			
Priority u	nder 35 U.S.C. § 119					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 						
2) 🔲 Notic 3) 🔲 Inform	t(s) e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/03 r No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:				

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DETAILED ACTION

Claim Rejections - 35 USC § 112

1. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

2. Claims 1 – 16 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. The independent claims 1 and 11 recite the limitation "the first and second data words" in lines 7 and 11, respectively. There is insufficient antecedent basis for this limitation in

the claim.

Claim 15 recites the limitation "the load". There is insufficient antecedent basis for this limitation in the claim.

Claim 16 recites the limitation "the precharge situation". There is insufficient antecedent basis for this limitation in the claim. It is also not clear what the term "precharge situation" means.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this

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subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

- 4. Claims 1 12 are rejected under 35 U.S.C. 102(b) as being anticipated by Hansen *et al.* (US Patent No. 6,006,318, hereinafter "Hansen").
- 5. With respect to claims 1, 7, 8, 10, 11, and 12, Hansen discloses a method for accessing a plurality of dynamic random access memory (DRAM) devices in parallel, each DRAM device having at least one memory bank, the method comprising (figure 13, col. 19, lines 16 25, see also figure 14, lines 31 36):

determining a distribution of data segments of the first and second data words in a plurality of memory banks (the segments are distributed among the 8 DRAM banks, note that Hansen discloses interleaving as well), the plurality of memory banks being among the memory banks of the plurality of DRAM devices (all 8 segments of the two words are stored in parallel);

determining a sequence of retrieving the data segments based on the plurality of memory banks (the sequence of retrieving the data segments is determined by the banks are arranged and is the same as the sequence of distribution for storage, i.e., sequencing of the four data segments/word stored in banks 1-4 are preserved so that when they are retrieved, words are put back together in the original byte order in which they were stored).

retrieving the data segments in parallel from the plurality of memory banks based on the distribution and the sequence (see col. 19, lines 30 - 35, two banks of memory modules are coupled in parallel for parallel retrieval of data segments); and

reassembling the retrieved data segments into the first and second data words (see col. 19, lines 19-25, the sixteen bit segments must be reassembled to form a 64-bit word).

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6. With respect to claim 2, receiving a retrieval request for the first data word and a retrieval request for the second data word, wherein the sequence can be different from the order of the retrieval requests received (order of a retrieval request has no effect in the sequence in which 16 bit segments of a word are stored in different bank of memory).

- 7. With respect to claims 3, 5 and 6, see figure 9. See also col. 4, lines 17 20.
- 8. With respect to claim 4, see figure 17.
- 9. With respect to claim 7, storing in parallel data segments of a data word into memory banks across a plurality of DRAM devices further comprises determining an in-bank burst length based upon the maximum word size (burst length is 1 for 64 bit words and 2 for 128 bit words), a total number of banks (4) in the plurality of DRAM devices and the data width of an individual bank (16 bits) and storing the data word in a burst having the in-bank burst length.
- 10. With respect to claim 8, each data segment is stored independently in different banks within a DRAM device.
- 11. With respect to claim 13, length of a burst is a multiple of the word size and the total number of banks and the data width within the bank are chosen based on the word size in Hansen. In Hansen, data segments are stored in parallel based on a multiple of a word size (2 words as shown in figure 14).

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- 12. With respect to claim 14, data segments belonging to one word are retrieved in one burst.
- 13. With respect to claim 15, the sequence is determined by how a word was loaded into the memory location.
- 14. With respect to claim 17, see rejection of claim 1. Memory I/O scheduler is independent of any other schedulers in other systems.

Conclusion

15. A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Woo H. Choi whose telephone number is (571) 272-4179. The examiner can normally be reached on M-F, 9:00-5:30.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Reginald Bragdon can be reached on (571) 272-4204. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Woo H. Choi

January 8, 2007